Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	46	((power adj failure) or reset) and (retention adj state)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/13 11:19
S2	1	((power adj failure) or reset) same initial\$6 same (retention adj state)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/16 17:03
S3	34	((power adj failure) or reset) and initial\$6 and (retention adj state)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/16 17:05
S4	2	((power adj failure) or reset) and initial\$6 and (retention adj state) and (memory adj controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/16 17:07
S5	21	(memory adj system) and (retention adj state)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/16 17:07
S6	62	memory same (retention adj state)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/16 17:13
S7	2	"714"/\$.ccls. and memory same (retention adj state)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/16 17:07
S8	42	memory same (retention adj state) and initial\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/16 17:14
S9	2	(memory adj controller) same (retention adj state) and initial\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT;	OR	ON	2004/01/20 15:07
			IBM_TDB			
S10	2	(memory adj controller) same (retention adj state)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/16 17:15

S11	6	(memory adj controller) same (delaying adj signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 09:13
S12	6571	(reset adj signal) same delay	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 09:24
S13	4048	delay\$3 with (reset adj signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 09:26
S14	15	(memory adj controller) same (delay\$3 with (reset adj signal))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 09:30
S15	382	(delay\$3 adj (reset adj signal))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 09:52
S16	2	(failure or fault or error) same memory same (delay\$3 adj (reset adj signal))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR:	ON	2004/01/20 09:31
S17	34	(delay\$3 adj (reset adj signal)) same initial\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 10:49
S18	373	(reset\$4 or (power adj (failure or error or fault))) same delay\$3 same (initial\$6 with memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 11:09
S19	6	(retention adj state) same delay	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 11:29
S20	314	(detect\$3 adj power adj failure) and reset\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 11:19

S21	162	(detect\$3 adj power adj failure) and reset\$3 and delay\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 11:24
S22	3	(detect\$3 adj power adj failure) same reset\$3 same delay\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 11:24
S23	261	retention adj state	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 11:29
S24	382	(delay\$3 adj (reset adj signal))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 15:16
S25	2	(retention adj state) and ((delay\$3 adj (reset adj signal)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 13:41
S26	8	(power adj failure) and (delay\$3 adj (reset adj signal))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 13:49
S27	2	(memory adj controller) same (delay\$3 adj reset adj signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 15:08
S28	239	714/22.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 15:11
S29	186	714/47.ccls. and ((power adj supply) or voltage)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 15:11
S30	146	714/22.ccls. and reset	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 15:12

S31	4	714/22.ccls. and (delay\$3 adj (reset adj signal))	US-PGPUB; USPAT; EPO; JPO; DERWENT;	OR	ON	2004/01/20 15:16
S32	56	714/24.ccls. and delay	IBM_TDB US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 17:10
S33	80	714/22.ccls. and delay	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 17:11
S34	5	714/24.ccls. and delay and (memory adj controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 17:11
S35	4	714/22.ccls. and delay and (memory adj controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 18:24
S36	2	"5182687".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/20 18:24
S37	18	714/22,24.ccls. and delay same (initializ\$6 or config\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/13 16:23
S38	114	714/22,24,47.ccls. and @pd>"20040120"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/09/10 15:19
S39	9	714/22,24,47.ccls. and @pd>"20040120" and (retention refresh)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/09/10 15:19
S40	299	@pd>"20040120" and ((retention refresh) adj (mode routine state))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/13 11:16

S54	262	@pd>"20040910" and ((retention refresh) adj (mode routine state))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/13 11:16
S56	12	714/22,24.ccls. and @pd>"20040910"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/13 11:21
S57	7	@pd>"20040910" and ((power adj failure) or reset) and (retention adj state)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/13 11:19
S58	72	714/22,24,47.ccls. and @pd>"20040910"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/13 11:21
S59	19772	@pd>"20040910" and (refresh retention)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/13 11:21
S61	54	714/22,24.ccls. and (voltage same (reset adj signal))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/13 11:23
S62	5823	(voltage same (reset adj signal)) and delay	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/13 11:23
S63	1485	(voltage same (reset adj signal)) same delay	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/13 11:23
S64	51	("714"/\$.ccls or "713"/\$.ccls.) and (voltage same (reset adj signal)) same delay	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/13 11:26
S65	7	713/340.ccls. and (voltage same (reset adj signal)) same delay	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/13 16:22

S66	531	713/340.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/13 16:22
S67	1418	714/22,24,47.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/13 16:23
S68	69	(S66 S67) and (reset same signal same delay)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/13 16:24
S69	180	(S66 S67) and (reset and signal and delay and voltage)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/13 16:24